PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of: Hiroshi Miyagi

U.S. Serial No.: 10/520,510

Group Art Unit: not yet assigned

U.S. Filing Date: not yet known

Examiner: not yet assigned

For: SEMICONDUCTOR INTEGRATED CIRCUIT AND SEMICONDUCTOR INTEGRATED MANUFACTURING METHOD

COMMUNICATION

EXPRESS MAIL LABEL NO: EV 482612705 US DATE OF DEPOSIT: JULY 14, 2005

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

The above-identified U.S. national phase application was filed on January 4, 2005 with an executed Declaration. However, the executed Declaration did not include a Power of Attorney. Therefore, applicant herewith submits a supplemental Declaration which includes a Power of Attorney. An Application Data Sheet is also submitted.

Respectfully submitted,

Date: July 14, 2005

Michael P. Dunnam Registration No. 32,611

Woodcock Washburn LLP One Liberty Place - 46th Floor Philadelphia, PA 19103 Telephone (215) 568-3100 Facsimile: (215) 568-3439